

## ABSTRACT OF THE DISCLOSURE

A display device including a plurality of display elements (500) arranged in a matrix, each display element including a display pixel (510) coupled to a switch (530), each display element including an addressable latch (540) having an output coupled to a controlling input of the switch, the addressable latch having a row address input (532) and a column address input (556). In one mode of operation, at least some display elements are activated at a first rate, and other display elements are activated at a second rate less than the first refresh rate by selectively addressing the display elements.